

## Supporting Information:

# Scalable Parallel Algorithm for Graph Neural Network Interatomic Potentials in Molecular Dynamics Simulations

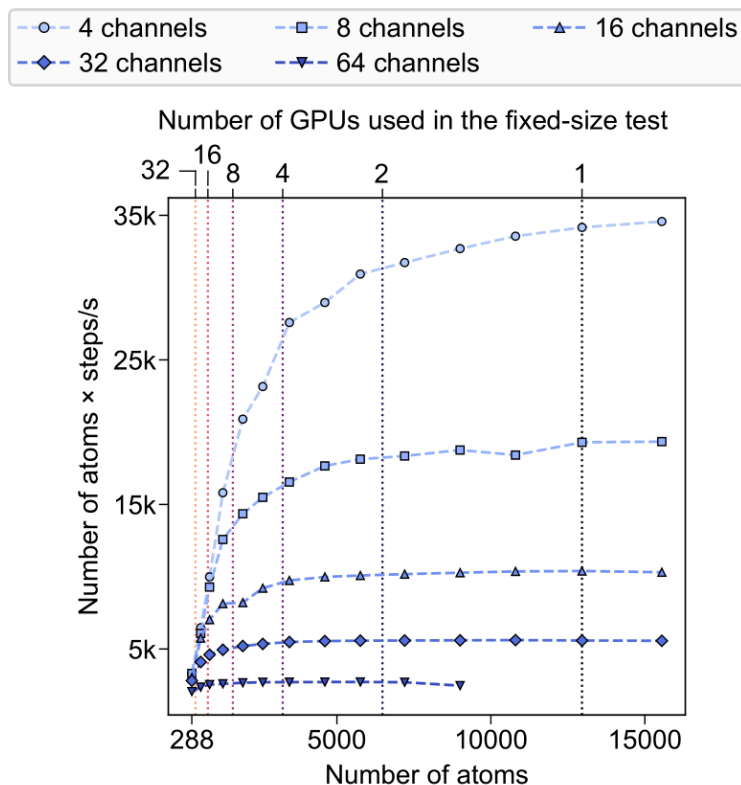
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## GPU utilization with respect to the number of atoms



**Figure S1.** Single GPU utilization curve of models with four message-passing layers and 4, 8, 16, 32, and 64 channels. The dotted lines indicate the number of atoms used in the fixed-size test.

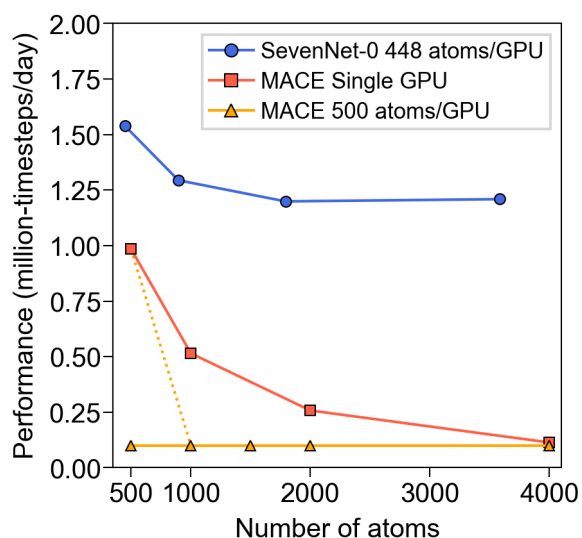
Figure S1 shows how the different number of channels and the number of atoms affects GPU utilization. The y-axis, the number of atoms multiplied by timesteps per second, indicates the amount of work (number of atoms multiplied by MD step) a GPU can yield in a unit of time (second). A plateau in this figure signifies full GPU utilization, achieved when a sufficient number of atoms is allocated. Conversely, a ballistic region observed at lower atom counts represents the GPU is in a suboptimal state. Notably, as the number of channels decreases, a greater number of atoms is needed to transition from this suboptimal state to the fully utilized state.

## Single GPU performance of SevenNet and NequIP

Considering the original NequIP is the baseline of our implementation, we compare a single GPU performance of SevenNet and NequIP. We use the same model hyperparameters of 32 channels, 4 message-passing layers, a maximum degree of representation ( $l_{\max}$ ) of 3, and a cutoff radius of 4.0 Å. The same benchmark script used in the scaled-size tests section is utilized. The benchmark runs span 210 MD steps, and we measure wall-clock time over the final 100 steps. The benchmark system involves alpha-quartz  $\text{SiO}_2$  containing 4,608 atoms. SevenNet, and NequIP achieved 1.99, and 1.12 timesteps per second, respectively.

For NequIP, we used the original implementation for training<sup>1</sup> and inference<sup>2</sup>. The SevenNet pair style is compiled with LAMMPS (23 Jun 2022 - Update 4), while the NequIP pair style uses a different version of LAMMPS (29 Sep 2021 - Update 2). Both pair styles utilize LibTorch from PyTorch/1.12.1. The compilation was done with nvcc compiler from CUDA/11.4, and gcc/8.3.0. The benchmarks are conducted on NVIDIA A100 80GB GPU in the KISTI Neuron cluster.

## Scaling of SevenNet and MACE



**Figure S2.** Weak-scaling performance of SevenNet-0 and MACE-MP-0<sup>3</sup>. The blue circles represent the performance of SevenNet in 448-atom amorphous Si<sub>3</sub>N<sub>4</sub>, from single to 8 GPUs. For MACE-MP-0, the red rectangle and yellow triangles indicate single-GPU and multi-GPU performance, respectively, measured with the high-entropy alloy system (500 atoms per GPU). The dashed line signifies the transition to multi-GPU in MACE. It is seen that the multi-GPU implementation of MACE becomes favorable only for very large-scale simulations due to significant additional costs. In contrast, SevenNet-0 well maintains the parallel efficiency regardless of the GPU number.

## References

- (1) Batzner, S.; Musaelian, A.; Sun, L.; Geiger, M.; Mailoa, J. P.; Kornbluth, M.; Molinari, N.; Smidt, T. E.; Kozinsky, B. *NequIP*, v0.5.6; Github:<https://github.com/mir-group/nequip> (accessed 2024-04-08).

(2) Batzner, S.; Musaelian, A.; Sun, L.; Geiger, M.; Mailoa, J. P.; Kornbluth, M.; Molinari, N.; Smidt, T. E.; Kozinsky, B. *Pair-Nequip*, v0.5.2; Github:[https://github.com/mir-group/pair\\_nequip](https://github.com/mir-group/pair_nequip) (accessed 2024-04-08).

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